CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC	0000 BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB	BBB RRRRRRR BBB RRR BBB RRRRRRR BBB RRRRRR	RRRRR TTTT RRRRR TTTT RRR RRR RRR RRR RR		LLL LLL LLL LLL LLL LLL LLL LLL LLL LL
--	--	--	--	--	--

....

00000000 00000000 00000000 00000000000	000000 00 00 00 00	88888888 88 88 88 88 88 88 88 88 88 88 88 88 888888	MM		QQQQQQ QQ QQ QQ QQ QQ QQ QQ QQ QQ QQ QQ
		\$			

012345678901234567890123456789

412344547490

* * * *

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.TITLE COBSMULQ_R8

COBOL Multiply Quadwords; File: COBMULQ.MAR

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FACILITY: COBOL ARITHMETIC

ABSTRACT:

This module contains the routine which multiplies two quadwords, producing a quadword result.

VERSION: 1

HISTORY:

AUTHOR:

John Sauter, 22-DEC-78

MODIFIED BY:

COBSMULQ_R8

```
M 15
COBSMULQ_R8
                                           COBOL Multiply Quadwords COB$MULQ_R8
                                                                                                   15-SEP-1984 23:46:26 VAX/VMS Macro V04-00 6-SEP-1984 10:48:20 [COBRTL.SRC]COBMULQ.MAR;1
                                                                            .SBTTL COBSMULQ_R8
                                                           95
96
97
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106
                                                                   FUNCTIONAL DESCRIPTION:
                                                  Multiplies two quadwords, producing a quadword result. There is no check for overflow; the low-order 64 bits
                                                                            are returned.
                                                                    CALLING SEQUENCE:
                                                                            JSB COB$MULQ_R8 (multiplier.rg.r, multiplicand.rg.r, product.wg.r)
                                                           108
                                                                            Arguments are passed in R6, R7 and R8.
                                                           110
                                                                    INPUT PARAMETERS:
                                                           111
                                                           112
                                                                                                            Value to the right of the * Value to the left of the *
                                                                            MULTIPLIER.rg.r
                                                                            MULTIPLICAND.rg.r
                                                           114
                                                                    IMPLICIT INPUTS:
                                                           115
                                                           116
                                                           117
                                                                            All of the trap bits in the PSL are assumed off.
                                                           118
                                                           119
                                                                    OUTPUT PARAMETERS:
                                                           PRODUCT.wg.r
                                                                                                             The result of the multiply.
                                                                    IMPLICIT OUTPUTS:
                                                                            NONE
                                                                    COMPLETION CODES:
                                                                            NONE
                                                                   SIDE EFFECTS:
                                                                            Destroys registers RO through R8.
                                                                 COB$MULQ_R8::
                                                                                      (R6),(R7),#0,R4; Multiply low half - Result to R4,R5
4(R6),(R7),R0; Form cross products
(R6),4(R7),R1;
                                                                            EMUL
                                            755C0101000005
                                     00 A66 66 51 67 166 50 54
                                                                            MULL3
                                                                            MULL3
ADDL2
                                                            140
                                                                                      R1, R0
#31, (R6), 10$
(R7), R0
#31, (R7), 20$
(R6), R0
                                                            141
                                                                                                                          Sum cross products
                                                                                                                          Compensate for unsigned bias
                                                                            BBC
                                                                            ADDL2
                                                                 10$:
                                                                            BBC
                                                                                                                          Compensate for unsigned bias
                                                                            ADDLZ
                                                            146
147
148
149
150
                                                                 20$:
                                                                                       RO,R5
                                                                                                                          Add in cross product
                                                                                       R4, (R8)
                                                                            MOVQ
                                                                                                                          Return result
                                                                            RSB
                                                                                                                          Return
                                                                            .END
```

N 15 COBSMULQ_R8 15-SEP-1984 23:46:26 6-SEP-1984 10:48:20 COBOL Multiply Quadwords VAX/VMS Macro V04-00 [COBRTL.SRC]COBMULQ.MAR;1 Page Symbol table COBSMULQ_R8 00000000 RG 01 Psect synopsis PSECT name Allocation PSECT No. Attributes ABS 00000000 USR LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE COBSCODE 00000027 LCL USR CON REL SHR NOWRT NOVEC LONG EXE RD Performance indicators Phase Page faults CPU Time **Elapsed Time** 00:00:00.43 00:00:02.21 00:00:02.12 00:00:00.00 00:00:00.89 00:00:00.00 00:00:00.00 Initialization 00:00:00.03 00:00:00.03 00:00:00.24 00:00:00.00 00:00:00.20 Command processing Pass 1 66 41 Symbol table sort Pass 2 Symbol table output Psect synopsis output 00:00:00.01 Cross-reference output

The working set limit was 900 pages.

1644 bytes (4 pages) of virtual memory were used to buffer the intermediate code.

There were 10 pages of symbol table space allocated to hold 1 non-local and 2 local symbols.

150 source lines were read in Pass 1, producing 8 object records in Pass 2.

O pages of virtual memory were used to define 0 macros.

+-----Macro library statistics !

Macro library name

Assembler run totals

Macros defined

_\$255\$DUA28:[SYSLIB]STARLET.MLB:2

0

O GETS were required to define O macros.

There were no errors, warnings or information messages.

MACRO/ENABLE=SUPPRESSION/DISABLE=(GLOBAL, TRACEBACK)/LIS=LIS\$:COBMULQ/OBJ=OBJ\$:COBMULQ MSRC\$:COBMULQ/UPDATE=(ENH\$:COBMULQ)

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